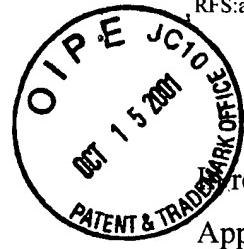


Receipt
#5

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Patent Application of: Kebichi et al.
Application No. 09/872,212

Filed: May 31, 2001

For: SYNCHRONIZATION POINT
• ACROSS DIFFERENT MEMORY BIST
CONTROLLERS

Examiner: Not yet assigned

Date: October 12, 2001

Art Unit: 2818

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper or fee referred to as being attached or enclosed) is being deposited with the United States Postal Service on 10/12/2001 as First Class Mail in an envelope addressed to: Office of Initial Patent Examination Customer Service Center, COMMISSIONER FOR PATENTS, Washington, DC 20231.

Attorney for Applicant(s)REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Office of Initial Patent Examination
Customer Service Center
COMMISSIONER FOR PATENTS
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Sir:

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Applicants have received the official Filing Receipt for the application referenced above, a copy of which (with requested correction shown in red ink) is attached as Exhibit A. Attached as Exhibit B is a copy of page 1 of Applicant's patent application as filed on May 31, 2001.

The following error appears on the Filing Receipt:

ERROR

**Under: Domestic Priority Data -
item omitted:**

CORRECT INFORMATION

This application claims benefit of: U.S. Provisional Application No. 60/261,991 filed on January 16, 2001, which claims benefit of U.S. Provisional Patent Application No. 60/248,337, filed on November 13, 2000. This application is a continuation-in-part of U.S. Patent Application No. 09/737,620, filed on December 14, 2000.

Applicants request that the identified error(s) be corrected and that a new official Filing Receipt be issued.

Please return the enclosed postcard to confirm that the items listed above have been received.

Please call the undersigned if any further information is required.

Respectfully submitted,

KLARQUIST SPARKMAN, LLP

By



Robert F. Scotti
Registration No. 39,830

One World Trade Center, Suite 1600
121 S.W. Salmon Street
Portland, Oregon 97204
Telephone: (503) 226-7391
Facsimile: (503) 228-9446

cc: Docketing



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UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/872,212 ✓	05/31/2001 ✓	2818	1158 ✓	1011-58140 ✓	9 ✓	36 ✓	5 ✓

CONFIRMATION NO. 1993

FILING RECEIPT

OC000000006371460

KLARQUIST SPARKMAN CAMPBELL LEIGH & WHINSTON, LLP
One World Trade Center
Suite 1600
121 S.W. Salmon Street
Portland, OR 97204

Date Mailed: 08/01/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Omar Kebichi, Wilsonville, OR;
Wu-Tung Cheng, Lake Oswego, OR; ✓
Christopher John Hill, Hampshire, UNITED KINGDOM; ✓
Paul J. Reuter, Northboro, MA; ✓
Yahya M.Z. Mustafa, Wilsonville, OR; ✓

PREVIOUSLY DOCKETED

Domestic Priority data as claimed by applicant

THIS APPLN CLAIMS BENEFIT OF 60/261,991 01/16/2001
WHICH CLAIMS BENEFIT OF 60/248,337 11/13/2000 ✓

and U.S. Patent Application 09/137,620 12/14/2000

Foreign Applications

If Required, Foreign Filing License Granted 08/01/2001

Projected Publication Date: Request for Non-Publication Acknowledged ✓

Non-Publication Request: Yes ✓

Early Publication Request: No ✓

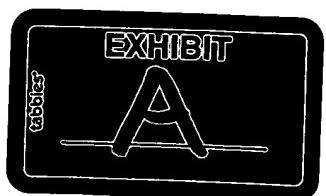
Title

Synchronization point across different memory BIST controllers ✓

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Preliminary Class

365

Data entry by : ROBINSON, FELICIA

Team : OIPE

Date: 08/01/2001

[REDACTED]

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NOT GRANTED

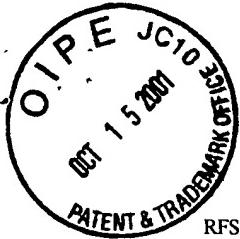
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- The articles such as "a," "an" and "the" are not included as the first words in the title of an application. They are considered to be unnecessary to the understanding of the title.
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DATE OF DEPOSIT: May 31, 2001

- 1 -

SYNCHRONIZATION POINT ACROSS DIFFERENT
MEMORY BIST CONTROLLERS

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RELATED APPLICATION DATA

Technology Center 2100

5

This application claims priority to the following U.S. applications:

- 1) U.S. Provisional Patent Application No. 60/261, 991, filed January 16, 2001.
- 2) U.S. Patent Application 09/737,620, filed December 14, 2000.
- 3) U.S. Provisional Patent Application No. 60/248,337 filed November 13,

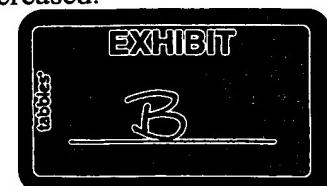
10 2000.

TECHNICAL FIELD

This invention generally relates to built-in self testing of electronic circuits and,
15 more particularly, to built-in self testing of memory circuits such as embedded memories within an integrated circuit, memories on a multichip module, or off-chip memories.

BACKGROUND

20 Many of today's integrated circuit (IC) designs are a complete system on a chip (SOC) that includes a processor core, multiple embedded memories, logic, I/O ports, etc. Embedded memories are the densest components in a SOC, accounting for up to 90% of the chip area. Memories are also the most sensitive to manufacturing process 25 defects, making it essential to thoroughly test them in a SOC. As IC's are produced with greater levels of circuit density, efficient testing schemes that guarantee very high fault coverage while minimizing test costs and chip area overhead have become essential. However, as the complexity of circuits continues to increase, and as the number of memory circuits that can be packed into a chip of a given size increases, the 30 need for efficient and cost effective testing of embedded memories has increased.





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Bib Data Sheet

CONFIRMATION NO. 1993

SERIAL NUMBER 09/872,212	FILING DATE 05/31/2001 RULE)	CLASS 365 714	GROUP ART UNIT 2818 2133	ATTORNEY DOCKET NO. 1011-58140
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APPLICANTS

Omar Kebichi, Wilsonville, OR;
 Wu-Tung Cheng, Lake Oswego, OR;
 Christopher John Hill, Hampshire, UNITED KINGDOM;
 Paul J. Reuter, Northboro, MA;
 Yahya M.Z. Mustafa, Wilsonville, OR;

**** CONTINUING DATA *******

THIS APPLN CLAIMS BENEFIT OF 60/261,991 01/16/2001
 AND A CIP OF 09/737,620 12/14/2000
 WHICH CLAIMS BENEFIT OF 60/248,337 11/13/2000

YES

**** FOREIGN APPLICATIONS *********IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

** 08/01/2001

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no				
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Guy Ramane <i>Signature</i> Initials				
		STATE OR COUNTRY OR	SHEETS DRAWING 9	TOTAL CLAIMS 36	INDEPENDENT CLAIMS 5

ADDRESS

KLARQUIST SPARKMAN CAMPBELL LEIGH & WHINSTON, LLP
 One World Trade Center
 Suite 1600
 121 S.W. Salmon Street
 Portland ,OR 97204

TITLE

Synchronization point across different memory BIST controllers

FILING FEE RECEIVED 1288	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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